| Form PTO-1449 (Rev. 2-32) | U.S. Department of Commerce Patent & Trademark Office | Atty. Docket No. Q61995 | Serial No. 09/726,114 |
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| INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) | | Applicant: Shigetaka KUMASHIRO | |
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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

| | | Document | Date | Country | Class | Sub-class | Translation Yes/No |
|---|---|---|---|---|---|---|---|
| DWC | | 7-202219 | 08/04/95 | Japan | | | |
| DWC | | 11-97676 | 04/09/99 | Japan | | | |
| DWC | | 2803543 | 07/17/98 | Japan | | | |
| DWC | | 2962346 | 08/06/99 | Japan | | | |
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

| | | |
|---|---|---|
| DWC | | Y. Cheng et al., "A Physical and Scalable I-V Model in BSIM3v3 for Analog/Digital Circuit Simulation", IEEE Transactions on Electron Devices, Vol. 44, No. 2, February 1997, pp. 277-287 with Abstract. |
| DWC | | E. Vogel et al., "Modeled Tunnel Currents for High Dielectric Constant Dielectrics", IEEE Transactions on Electron Devices, Vol. 45, No. 6, June 1998, pp. 1350-1355 with Abstract. |
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